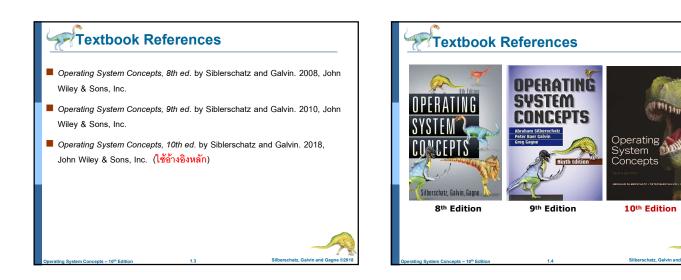
## CS204341- Operating System ระบบปฏิบัติการ

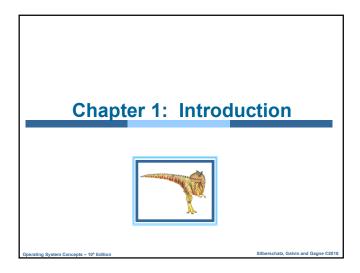
Sec.001 Varin Chouvatut (ผศ.ดร.วาริน เชาวทัศ) Office: CSB109 Email: varin.ch@cmu.ac.th

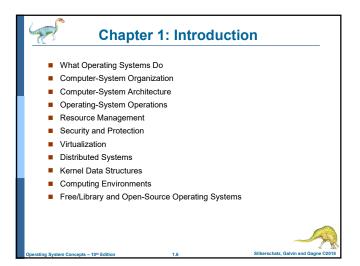
Sec.002 Worawut Srisukkham (อ.ดร.วรวุฒิ ศรีสูบกำ) Office: CSB107 Email: worawut.s@cmu.ac.th

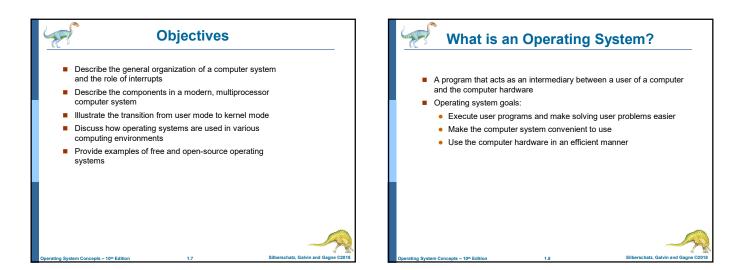
> Department of Computer Science Faculty of Science Chiang Mai University

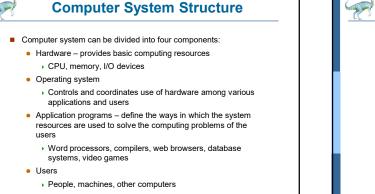


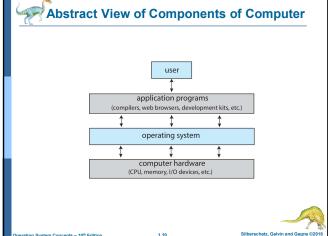








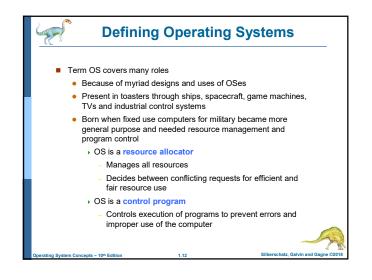


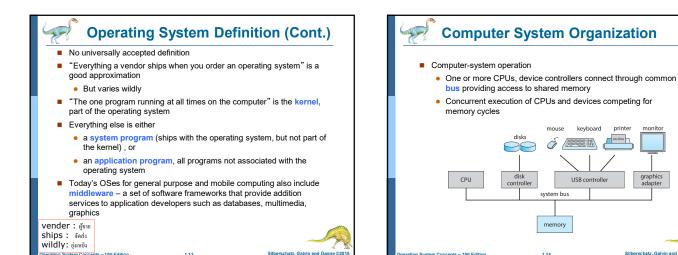


## What Operating Systems Do

Depends on the point of view

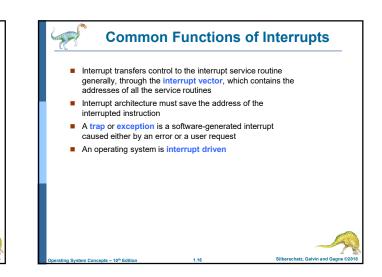
- Users want convenience, ease of use and good performance
- Don't care about resource utilization
- But shared computer such as mainframe or minicomputer must keep all users happy
   Operating system is a resource allocator and control program
  - making efficient use of HW and managing execution of user programs
- Users of dedicate systems such as workstations have dedicated resources but frequently use shared resources from servers
- Mobile devices like smartphones and tables are resource poor, optimized for usability and battery life
- Mobile user interfaces such as touch screens, voice recognition
- Some computers have little or no user interface, such as embedded computers in devices and automobiles
  - Run primarily without user intervention

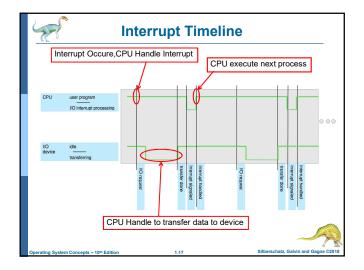


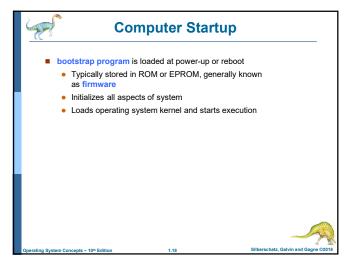


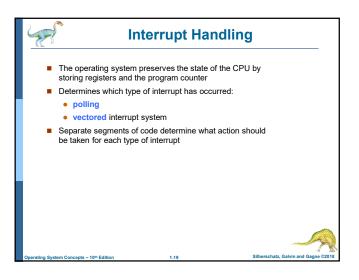


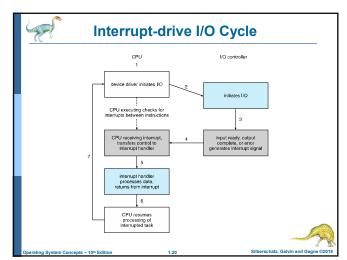
- I/O devices and the CPU can execute concurrently
- Each device controller is in charge of a particular device type
- Each device controller has a local buffer
- Each device controller type has an operating system device driver to manage it
- CPU moves data from/to main memory to/from local buffers
- I/O is from the device to local buffer of controller
- Device controller informs CPU that it has finished its operation by causing an interrupt

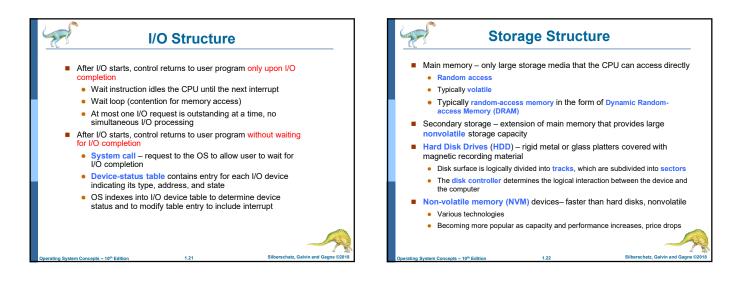


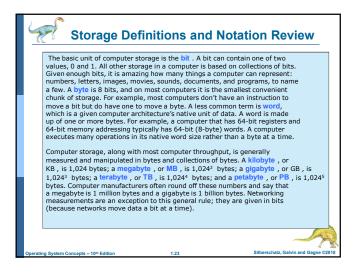


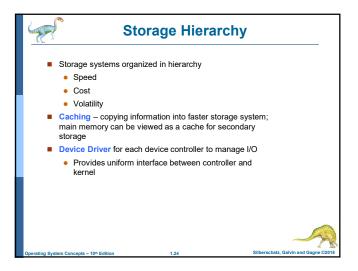


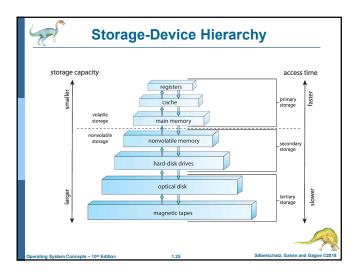


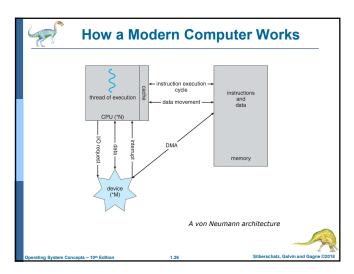


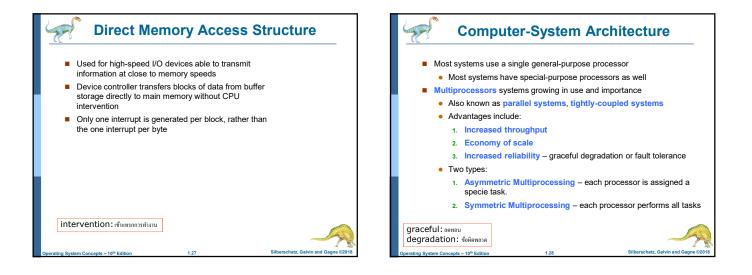


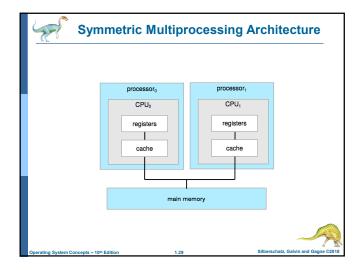


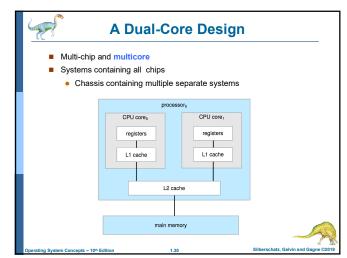


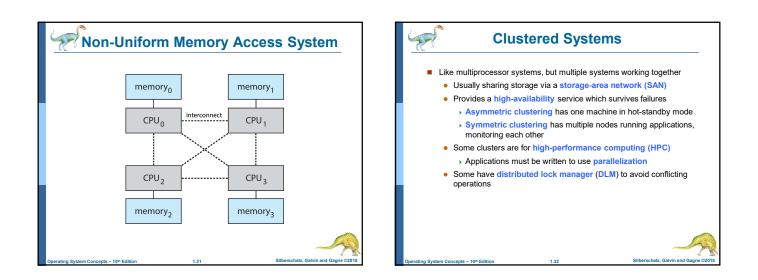


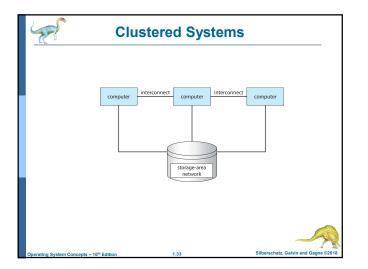


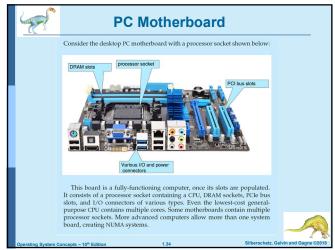


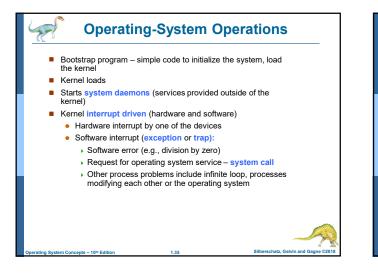


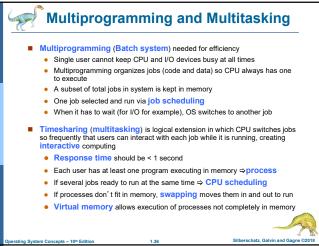


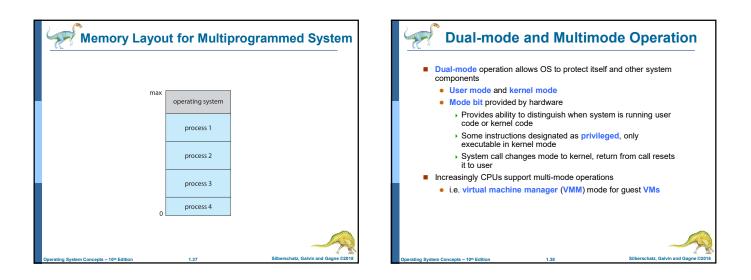


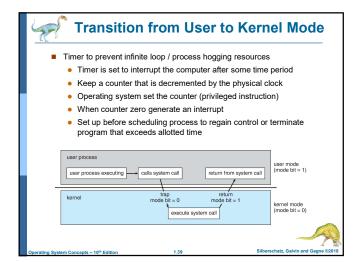


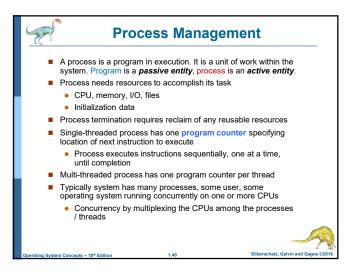


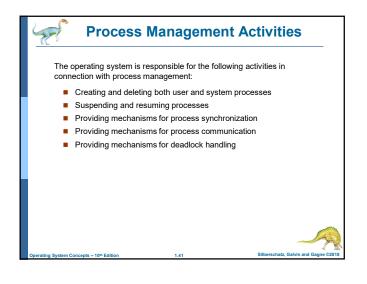


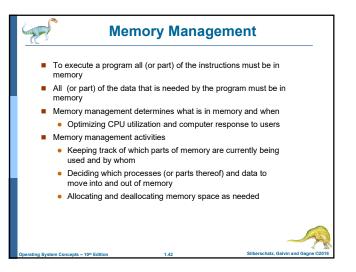


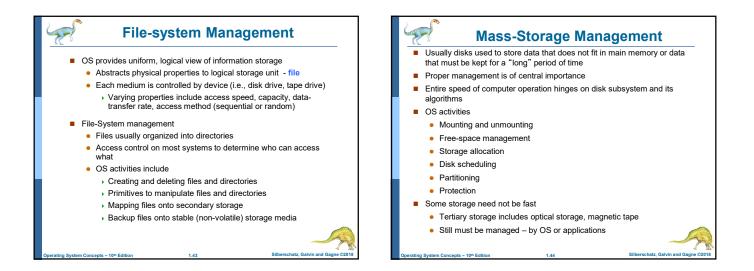




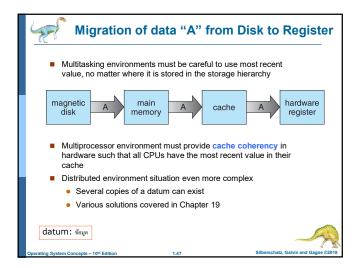


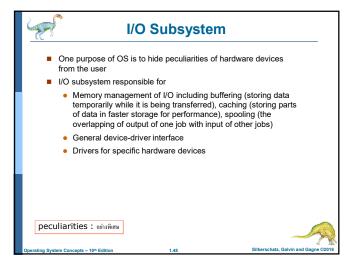






Cachir	ng	4	<b>e</b> Cha	racteris	tics of	Various	Types o	of Storag
Important principle, performed at mar			_evel	1	2	3	4	5
(in hardware, operating system, softwork) Information in use copied from slower	,		Vame Fypical size	< 1 KB	cache < 16MB	< 64GB	solid-state disk < 1 TB	magnetic disk < 10 TB
temporarily Faster storage (cache) checked first t information is there	o determine if		mplementation echnology	custom memory with multiple ports CMOS	on-chip or off-chip CMOS SRAM	CMOS SRAM	flash memory	magnetic disk
<ul> <li>If it is, information used directly from the second second</li></ul>	om the cache (fast)		Access time (ns)	0.25-0.5	0.5-25	80-250	25,000-50,000	5,000,000
<ul> <li>If not, data copied to cache and u</li> </ul>	· · /		Bandwidth (MB/sec)	20,000-100,000	5,000-10,000	1,000-5,000	500	20-150
Cache smaller than storage being cache			Managed by	compiler	hardware	operating system	operating system	operating system
<ul> <li>Cache management important de</li> </ul>			Backed by	cache	main memory	disk	disk	disk or tape
Cache size and replacement polic	0 1		Moveme	nt between le	vels of stor	age hierarchy	can be expli	cit or implicit
			explicit :		ເໃນ			
m Concepts - 10 <sup>th</sup> Edition 1.45	Silberschatz, Galvin and Gagne ©2018	0	ting System Concepts -	A OT T AND	1.4		Silbered	hatz. Galvin and Gao

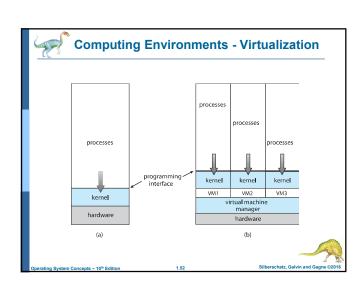


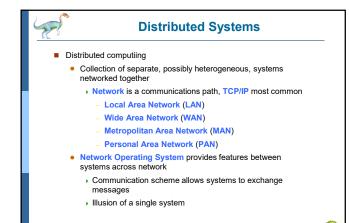


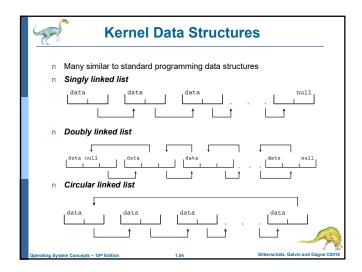


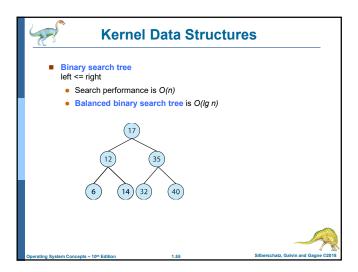


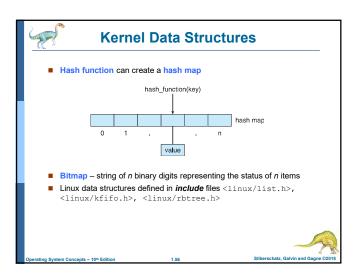
- Apple taptop running wat OS X host, windows as a guest
   Developing apps for multiple OSes without having multiple systems
- QA testing applications without having multiple systems
- Executing and managing compute environments within data centers
- VMM can run natively, in which case they are also the host • There is no general purpose host then (VMware ESX and
- Citrix XenServer)

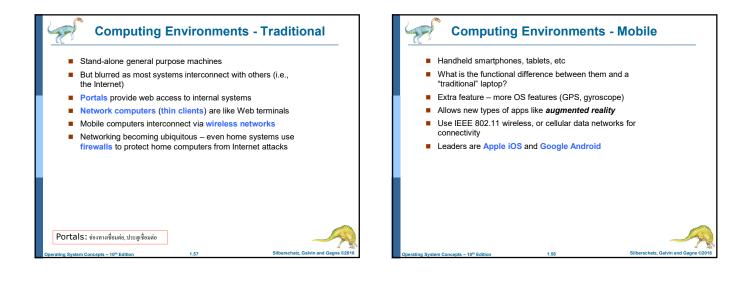


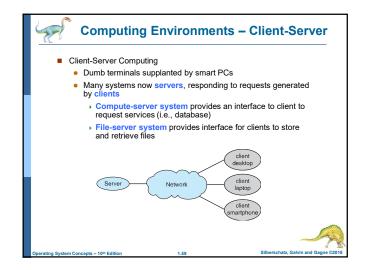


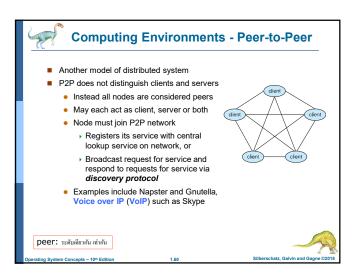


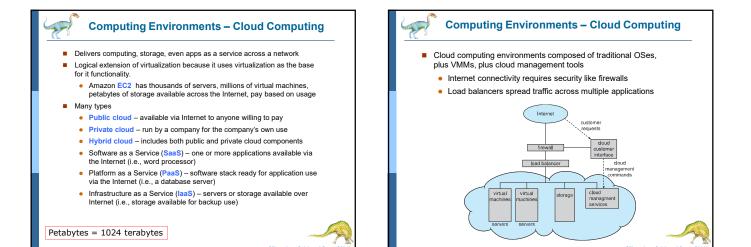


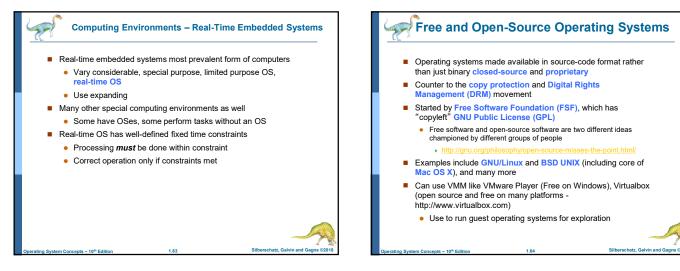


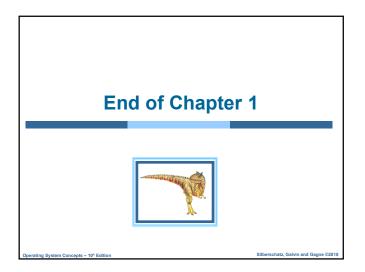












ภาควิชาวิทยาการคอมพิวเตอร์ คณะวิทยาศาสตร์ มหาวิทยาลัยเชียงใหม่